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	Application No.	Applicant(s)	<del></del>
A	09/912,064	OKA ET AL.	
Notice of Allowability	Examiner	Art Unit	
	Fazli Erdem	2826	
The MAILING DATE of this communication appeall claims being allowable, PROSECUTION ON THE MERITS IS therewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R	(OR REMAINS) CLOSED in this ap or other appropriate communicatio IGHTS. This application is subject	oplication. If not included in will be mailed in due co	urse. <b>THIS</b>
1. X This communication is responsive to 7/28/2005.			
2. The allowed claim(s) is/are <u>3,6,8,10,12-16,19 and 21-27.</u>		. /	
3. $igotimes$ The drawings filed on <u>25 July 2001</u> are accepted by the Ex	kaminer.		
<ul> <li>4.  Acknowledgment is made of a claim for foreign priority ur</li> <li>a)  All b)  Some* c)  None of the:</li> <li>1.  Certified copies of the priority documents have</li> <li>2.  Certified copies of the priority documents have</li> <li>3.  Copies of the certified copies of the priority do</li> <li>International Bureau (PCT Rule 17.2(a)).</li> <li>* Certified copies not received:</li> </ul>	SUPERVISO e been received. TECHNO e been received in Application No	RY PATENT EXAMINER LOGY CENTER 2800	n from the
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		ocomplying with the requi	rements
5. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give			FICE OF
6. CORRECTED DRAWINGS ( as "replacement sheets") must (a) including changes required by the Notice of Draftspers 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Paper No./Mail Date  Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in the second sheet is should be labeled as such in the second sheet in the second sheet in the second sheet in the second sheet is should be labeled as such in the second sheet in the second sheet in the second sheet is should be labeled as such in the second sheet in the second sheet in the second sheet is should be labeled as such in the second sheet in the second sheet in the second sheet is should be labeled as such in the second sheet in the second sheet in the second sheet is sheet in the second sheet in the seco	son's Patent Drawing Review(PTO . s Amendment / Comment or in the 6 .84(c)) should be written on the drawi	Office action of ings in the front (not the ba	ack) of
7. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT			e the
Attachment(s)  I. Notice of References Cited (PTO-892)  2. Notice of Draftperson's Patent Drawing Review (PTO-948)  3. Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date  4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview Summary Paper No./Mail Da 98), 7. ☐ Examiner's Amend	nte	
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## **DETAILED ACTION**

## Allowable Subject Matter

1. Claims 3,6,8, 10, 12-16, 19 and 21-27 allowed.

2. The following is an examiner's statement of reasons for allowance. Prior art failed to establish a semiconductor chip package comprising a first semiconductor chip and a second semiconductor chip, first chip being mounted to the second chip with the reverse side of the second chip facing the reverse side of the first chip, a nonconductive interposer having a through-hole extending from a first surface to the second surface where the hole is bigger than the second chip size, an adhesive sheet formed of a sheet-shaped adhesive material provided on the interposer at the first surface of the interposer to cover the through-hole, the second surface of the adhesive sheet being exposed through the through-hole from a side of the interposer substrate at the second surface of the interposer substrate where the reverse side of the second integrated semiconductor chip is fixed to the second surface of the adhesive sheet and the reverse side of the first integrated chip is fixed to the first surface of the adhesive sheet as to face the reverse side of the second integrated semiconductor chip at a position at which the second integrated semiconductor chip is fixed where the second electrode can be wired to external terminals on the second surface of the interposer substrate.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## Conclusion

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Fazli Erdem whose telephone number is (571) 272-1914. The examiner can normally be reached on M - F 8:00 - 5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nathan Flynn can be reached on (571) 272-1915. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

FE September 30, 2005